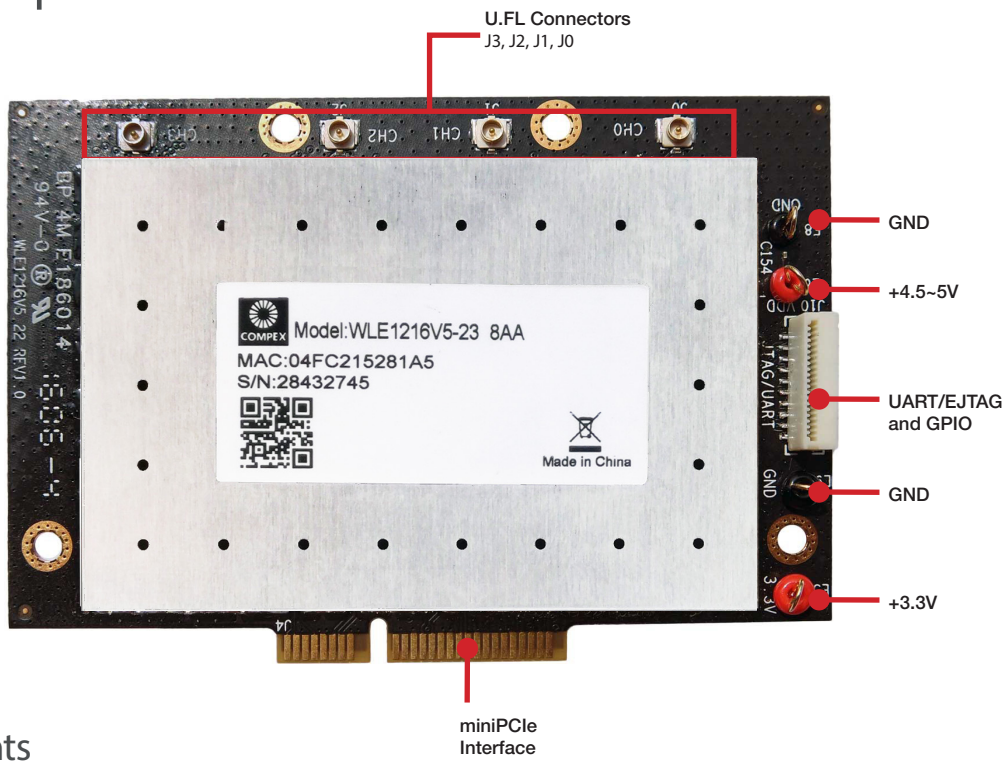
Component Map



Power Requirements

DC Power	3.3V for Module and 5V for PA
Power Consumption	13 Watts (Max)

Mini PCIe Slot Pin Assignment

Top side		Bottom side	
1	WAKE_L	2	+3.3V
3	CHIP_PWD_L	4	GND
5	NC	6	NC
7	CLKREQ_L	8	NC
9	GND	10	NC
11	REFCLK-	12	NC
13	REFCLK+	14	NC
15	GND	16	NC
Mechanical key			
17	NC	18	GND
19	NC	20	W_DISABLE_N
21	GND	22	PERST_L
23	PERn0	24	+3.3V
25	PERp0	26	GND
27	GND	28	NC
29	GND	30	NC
31	PETn0	32	NC
33	PETp0	34	GND
35	GND	36	NC
37	NC	38	NC
39	NC	40	GND
41	NC	42	NC
43	GND	44	LED_WLAN_L
45	+5V	46	NC
47	+5V	48	NC
49	+5V	50	GND
51	+5V	52	+3.3V

GPIO Pin Mapping

UART/EJTAG and GPIO Function					
Pin	GPIO	Function	Pin	GPIO	Function
1	GPIO_30	GLOBAL_TEST	2	GPIO_29	CPU_EJTAG_SEL
3	GPIO_16	WARM RESET/ EJTAG RESET	4	NC	GND
5	GPIO_12	TMS	6	GPIO_6	SOC_UART_RXD
7	NC	GND	8	NC	GND
9	GPIO_13	TCK	10	GPIO_7	SOC_UART_TXD
11	NC	GND	12	NC	GND
13	GPIO_14	TDI	14	GPIO_8	SOC_UART_RTS_N
15	NC	GND	16	NC	GND
17	GPIO_15	TDO	18	GPIO_9	SOC_UART_CTS_N
19	NC	GND	20	NC	GND
BOOTSTRAP					
	GPIO_15	REF_CLK_SEL(LOW)		GPIO_16	CPU_INIT_ RST_N(HIGH)
	GPIO_29	CPU_EJTAG_ SEL(HIGH)		GPIO_30	GLOBAL_TEST(LOW)
	GPIO_31	CHIP_TEST_ MODE(LOW)		GPIO_32	DIG_TEST_EN(LOW)
	GPIO_17	WLAN_LED		GPIO_18	WLAN_DISABLE_N
	GPIO_19	EEPROM_PROT		GPIO_20	I2C_SDA
	GPIO_22	I2C_CLK		GPIO_0	SWCOM2
	GPIO_1	SWCOM3		GPIO_2	SWCOM0
	GPIO_3	SWCOM1		GPIO_4	SWCOM4
	GPIO_5	SWCOM5			

* NC above denotes not connected to GPIO.