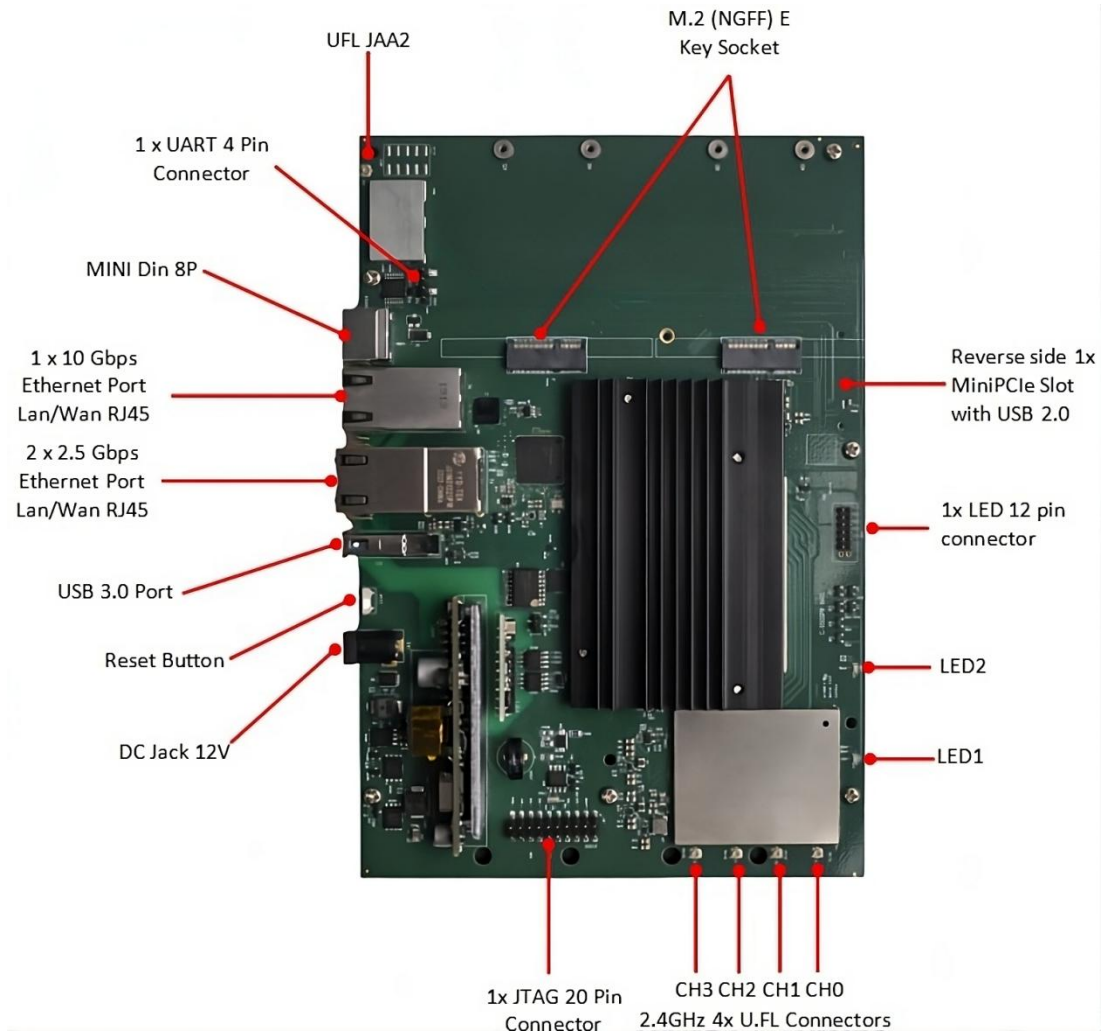


Component Map



Power Requirements

DC power	12V
Power Consumption	21.5W

M.2 Slot Pin Assignment

M.2 Slot Pin Assignment J3			
Top side		Bottom side	
1	GND	2	VDD_3V3
3	NC	4	VDD_3V3
5	NC	6	NC
7	GND	8	NC
9	NC	10	NC
11	NC	12	NC
13	NC	14	WSI_DAT_IN
15	VDD_XPA_PCIE2	16	PCIE2_LED1
17	VDD_XPA_PCIE2	18	GND
19	VDD_XPA_PCIE2	20	WSI_CLK_IN
21	VDD_XPA_PCIE2	22	NC
23	VDD_XPA_PCIE2		
Mechanical key E			
33	GND	32	WSI_DAT_OUT
35	PCIE2_TX0_P	34	NC
37	PCIE2_TX0_N	36	WSI_CLK_OUT
39	GND	38	NC
41	PCIE2_RX0_P	40	NC
43	PCIE2_RX0_N	42	NC
45	GND	44	NC
47	PCIE2_REFCLK_P	46	NC
49	PCIE2_REFCLK_N	48	NC
51	GND	50	NC
53	CLKREQ_3P3_N	52	PERST_3P3
55	WAKE_3P3_N	54	NC
57	GND	56	WDIS_PCIE2
59	PCIE2_TX1_P	58	NC
61	PCIE2_TX1_N	60	NC
63	GND	62	NC
65	PCIE2_RX1_P	64	NC
67	PCIE2_RX1_N	66	NC
69	GND	68	NC
71	NC	70	NC
73	NC	72	VDD_3V3
75	GND	74	VDD_3V3

M.2 Slot Pin Assignment J4			
Top side		Bottom side	
1	GND	2	VDD_3V3
3	NC	4	VDD_3V3
5	NC	6	NC
7	GND	8	NC
9	NC	10	NC
11	NC	12	NC
13	NC	14	WSI_DAT_OUT_PCIE2
15	VDD_XPA_PCIE0	16	NC
17	VDD_XPA_PCIE0	18	GND
19	VDD_XPA_PCIE0	20	WSI_CLK_OUT_PCIE2
21	VDD_XPA_PCIE0	22	NC
23	VDD_XPA_PCIE0		
Mechanical key E			
33	GND	32	WSI_DAT_OUT_PCIE0
35	PCIE0_TX0_P	34	NC
37	PCIE0_TX0_N	36	WSI_CLK_OUT_PCIE0
39	GND	38	NC
41	PCIE0_RX0_P	40	NC
43	PCIE0_RX0_N	42	NC
45	GND	44	BT_PRIORITY
47	REFCLK_P	46	BT_WLAN
49	REFCLK_N	48	BT_ACTIVE
51	GND	50	NC
53	CLKREQ_3P3_N	52	PERST_3P3
55	WAKE_3P3_N	54	NC
57	GND	56	WDIS_PCIE0
59	NC	58	NC
61	NC	60	NC
63	GND	62	NC
65	NC	64	NC
67	NC	66	NC
69	GND	68	NC
71	NC	70	NC
73	NC	72	VDD_3V3
75	GND	74	VDD_3V3

JTAG Interface Pin Assignment

JTAG Interface Pin Assignment J1000			
Pin	Signal	Pin	Signal
1	DVDD_1V8	2	DVDD_1V8
3	JTAG_TRSTN	4	NC
5	JTAG_TDI	6	GND
7	JTAG_TMS	8	GND
9	JTAG_TCK	10	GND
11	DVDD_1V8	12	GND
13	JTAG_TDO	14	GND
15	JTAG_SRSTN	16	GND
17	GND	18	GND
19	GND	20	GND

UART Pin Assignment J1003	
Pin	Signal
1	DVDD_3V3
2	UART_TXD1
3	UART_RXD1
4	GND

MINI_DIN8 Pin Assignment J8009	
Pin	Signal
1	NC
2	NC
3	DIN8_TX
4	NC
5	DIN8_RX
6	DTR_IN
7	NC
8	NC
9	GND
10	GND
11	GND

USB 3.0 Port Pin Assignment

USB 3.0 Port Pin Assignment J11			
Pin	Signal	Pin	Signal
1	DVDD_USB	2	USB0_CONN_DM
3	USB0_CONN_DP	4	GND
5	USB0_SS_RX_N	6	USB0_SS_RX_P
7	GND	8	CONN_USB0_SS_TX_N
9	CONN_USB0_SS_TX_P	10	GND
11	DVDD_USB		

LED Pin Assignment

LED Header Pin Assignment			
Pin	Signal	Pin	Signal
1	LED1_R_L	2	DVDD_3V3
3	LED1_G_L	4	DVDD_3V3
5	LED2_R_L	6	DVDD_3V3
7	LED2_G_L	8	DVDD_3V3
9	LED1_B_L	10	DVDD_3V3
11	SW_RST	12	GND
13	LED2_B_L	14	DVDD_3V3

Ethernet LAN Port Pin Assignment

Ethernet LAN Port (RJ45 10G) P1			
Pin	Signal	Pin	Signal
P6	GND	P4	AQ_MD_B_P
P5	AQ_MD_B_N	P2	AQ_MD_C_N
P3	AQ_MD_C_P	P1	NC
P12	GND	P11	AQ_MD_A_P
P10	AQ_MD_A_N	P8	AQ_MD_D_P
P9	AQ_MD_D_N	P7	NC
VEE	GND	P13	DATA2-2_POE
		P14	DATA2-1_POE
		P15	DATA1-2_POE
		P16	DATA1-1_POE

Ethernet LAN Port(RJ45 2.5 x 2 PORT) P2			
Pin	Signal	Pin	Signal
1	P2_TRXN0	21	P3_TRXN0
2	P2_TRXP0	22	P3_TRXP0
3	P2_TRXN1	23	P3_TRXN1
4	P2_TRXP1	24	P3_TRXP1
5	P2_TRXN2	25	P3_TRXN2
6	P2_TRXP2	26	P3_TRXP2
7	P2_TRXN3	27	P3_TRXN3
8	P2_TRXP3	28	P3_TRXP3
9	VDD_1V8	29	VDD_1V8
10	VDD_1V8	30	VDD_1V8
11	DATA2-2_POE	31	NC
12	DATA2-1_POE	32	NC
13	DATA1-2_POE	33	DATA1-1_POE
14	DATA1-1_POE	34	DATA1-2_POE
38	DVDD_3V3	42	DVDD_3V3
37	LED-	41	LED-
36	DVDD_3V3	40	DVDD_3V3
35	LEDR-	39	LEDR-
15	GND	18	GND
16	GND	19	GND
17	GND	20	GND

GPIO Pin Mapping

GPIO PIN	Function	GPIO Pin	Function
GPIO_0	QSPI_DATA[3]	GPIO_30	PCIE2_WAKE_N
GPIO_1	QSPI_DATA[2]	GPIO_31	SW_RST
GPIO_2	QSPI_DATA[1]	GPIO_32	LED1_B
GPIO_3	QSPI_DATA[0]	GPIO_33	EEPROM_SCL
GPIO_4	QSPI_CS_N	GPIO_34	UART_RXD1
GPIO_5	QSPI_CLK	GPIO_35	UART_TXD1
GPIO_6	NC	GPIO_36	AQR_RST
GPIO_7	NC	GPIO_37	MHN_INTN
GPIO_8	NC	GPIO_38	IPQ_MDC
GPIO_9	NC	GPIO_39	IPQ_MDIO
GPIO_10	NC	GPIO_40	BT_DIO21
GPIO_11	SPI0_CLK	GPIO_41	BT_DIO22
GPIO_12	SPI0_CS	GPIO_42	BT_DIO23
GPIO_13	SPI0_MISO	GPIO_43	BT_DIO24
GPIO_14	SPI0_MOSI	GPIO_44	BT_RST
GPIO_15	EX_CLK	GPIO_45	MX_SCALE
GPIO_16	EX_DATA	GPIO_46	EEPROM_SDA
GPIO_17	EX_INT	GPIO_47	CX_SCALE
GPIO_18	LED2_R	GPIO_48	RTC_SCL
GPIO_19	LED2_B	GPIO_49	RTC_SDA
GPIO_20	LED2_G	GPIO_50	WDIS_PCIE0
GPIO_21	LED1_R	GPIO_51	WDIS_PCIE2
GPIO_22	PCIE0_CLKREQ_N	GPIO_52	UART_RX
GPIO_23	PCIE0_RST_N	GPIO_53	UART_TX
GPIO_24	PCIE0_WAKE_N	GPIO_54	IPQ_PTA1_1
GPIO_25	TPYE_MECO_R	GPIO_55	IPQ_PTA1_2
GPIO_26	LED1_G	GPIO_56	IPQ_PTA1_0
GPIO_27	USB_FLAG_OC	GPIO_57	APC_SCALE
GPIO_28	PCIE2_CLKREQ_N	GPIO_58	2G_IRON_RST
GPIO_29	PCIE2_RST_N	GPIO_59	QFPROM_EN
		GPIO_60	MHN_PST_N
		GPIO_61	IPQ_PTA2_0
		GPIO_62	IPQ_PTA2_1
		GPIO_63	IPQ_PTA2_2
		GPIO_64	DC_IN

miniPCIe Slot Pin Assignment			
Pin	Signal	Pin	Signal
1	EPAPER_WAKE	2	DVDD_3V3
3	EPAPER_BT_ACTIVE	4	GND
5	EPAPER_WLAN_ACTIVE	6	VDD_XPA_PCIE0
7	EPAPER_CLKREQ	8	EPAPER_BT_PRIORITY
9	GND	10	EPAPER_BT_FREQ
11	NC	12	EPAPER_UIM_CLK
13	NC	14	EPAPER_UIM_RESET
15	GND	16	EPAPER_UIM_VPP
Mechanical key E			
17	EPAPER_UIM_C8	18	GND
19	EPAPER_UIM_C4	20	EPAPER_W_DISABLE
21	GND	22	EPAPER_PERST
23	NC	24	DVDD_3V3
25	NC	26	GND
27	GND	28	VDD_XPA_PCIE0
29	GND	30	EPAPER_SMB_CLK
31	NC	32	EPAPER_SMB_DATA
33	NC	34	GND
35	GND	36	USB0_EPAPER_M
37	GND	38	USB0_EPAPER_P
39	DVDD_3V3	40	GND
41	DVDD_3V3	42	NC
43	GND	44	NC
45	NC	46	NC
47	NC	48	VDD_XPA_PCIE0
49	NC	50	GND
51	NC	52	DVDD_3V3