

Hardware Guide - WLTE7000E6

Component Map



M.2 E Key Interface with PCIe 3.0

Power Requirements

DC Power 3.3V

Power Consumption 9.3W (Max)

M.2 E Key Slot Pin Assignment

	Top side		Bottom side
1	GND	2	VDD_3V3
3	l NC	4	VDD_3V3
5	NC	6	NC
7	GND	8	NC
9	NC	10	NC
11	l NC	12	NC
13	NC	14	NC
15	NC NC	16	NC
17	NC	18	GND
19	l NC	20	NC
21	NC	22	NC
23	NC		
Mechanical key E			
33	GND	32	NC
35	PCIEO_TXO_P	34	l NC
37	PCIE0_TX0_N	36	NC
39	GND	38	NC NC
41	PCIEO_RXO_P	40	NC
43	PCIEO_RXO_N	42	NC NC
45	GND	44	NC
47	REFCLK_P	46	l NC
49	REFCLK_N	48	NC
51	GND	50	NC
53	CLKREQ_3V3	52	PERST_3V3
55	WAKE_3V3	54	NC
57	GND	56	W_DISABLE_1V8
59	PCIEO_TX1_P	58	NC
61	PCIE0_TX1_N	60	NC
63	GND	62	NC
65	PCIEO_RX1_P	64	NC
67	PCIEO_RX1_N	66	NC
69	GND	68	NC
71	NC	70	NC
73	NC	72	VDD_3V3
75	GND	74	VDD_3V3

