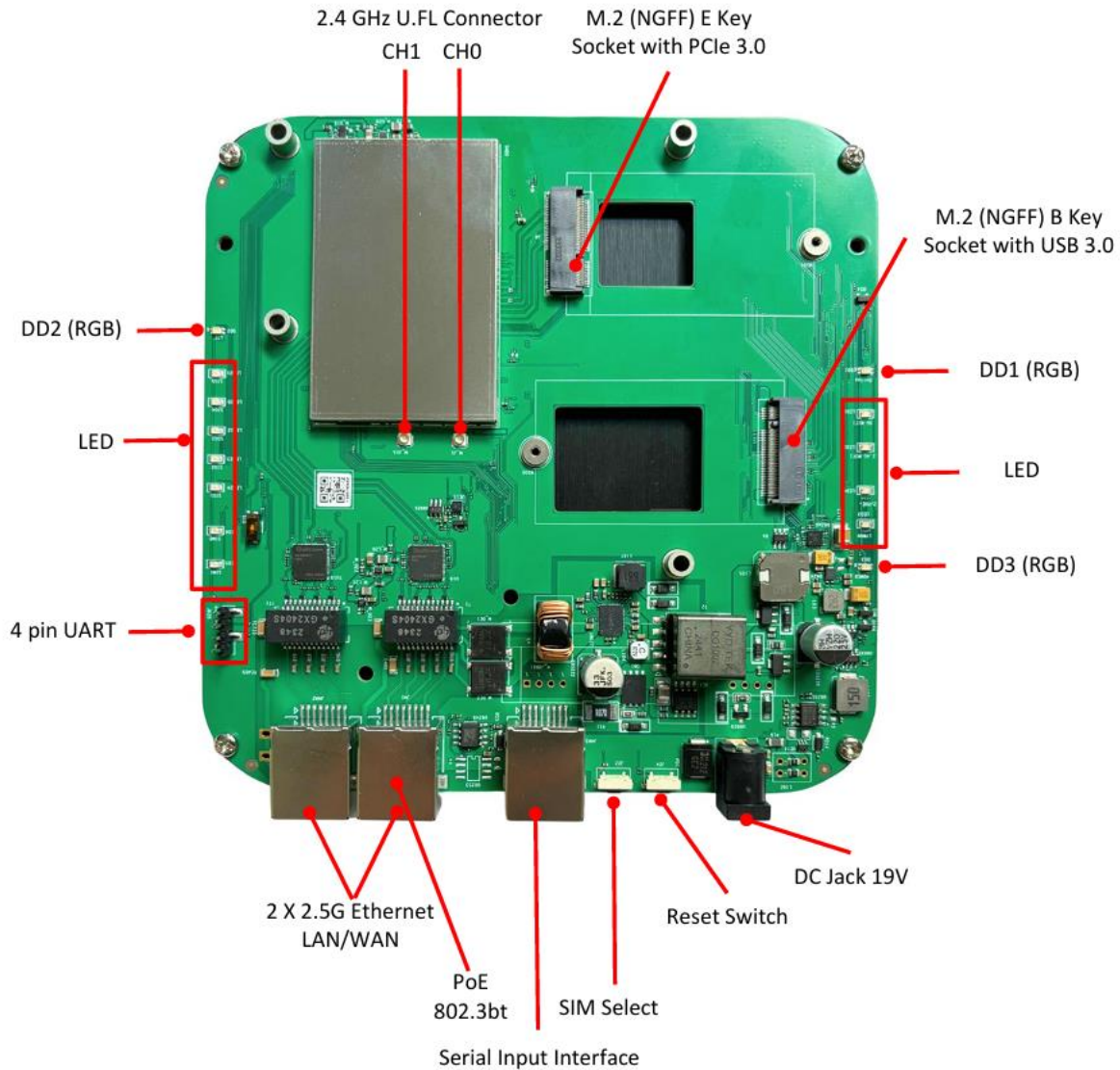


Component Map



Power Requirements

DC power	19V
Power Consumption	11.3W (Max)

GPIO Pin Mapping

GPIO PIN	Function	GPIO PIN	Function
GPIO_0	2G_PA_EN0	GPIO_30	SDA_3V3
GPIO_1	2G_LNA_EN0	GPIO_31	SIM_SELECT
GPIO_2	NC	GPIO_32	WDIS_PCIE1_5G
GPIO_3	NC	GPIO_33	USIM_CD
GPIO_4	2G_PA_EN1	GPIO_34	MEC_GPIO34
GPIO_5	2G_LNA_EN1	GPIO_35	WPS/INT
GPIO_6	NC	GPIO_36	INT_1V8
GPIO_7	NC	GPIO_37	INT_1V8
GPIO_8	NAND_DATA3	GPIO_38	MANUAL_RST
GPIO_9	NAND_DATA2	GPIO_39	NAPA_INT
GPIO_10	NAND_DATA1	GPIO_40	WSI_CLK_5>2
GPIO_11	NAND_DATA0	GPIO_41	WSI_DAT_5>2
GPIO_12	NAND_CS_N	GPIO_42	WSI_CLK_2>5
GPIO_13	NAND_CLK	GPIO_43	PCIE0_CLK_REQ
GPIO_14	SPI_CLK	GPIO_44	PCIE0_RSTN
GPIO_15	SPI_MOSI	GPIO_45	PCIE0_WAKE
GPIO_16	SPI_MISO	GPIO_46	PCIE1_CLKREQ_N
GPIO_17	SPI_CS_N	GPIO_47	PCIE1_RST_N_5G
GPIO_18	UART_RX	GPIO_48	PCIE1_WAKE_N_5G
GPIO_19	UART_TX	GPIO_49	WD_OE
GPIO_20	RST_OUT_N	GPIO_50	WDI_GPIO50
GPIO_21	core_voltage_control[0]	GPIO_51	NAPA_RST
GPIO_22	DTR_IN	GPIO_52	WSI_DAT_2>5
GPIO_23	core_voltage_control[1]		
GPIO_24	DC_IN		
GPIO_25	SM_SEL		
GPIO_26	RESET		
GPIO_27	NC		
GPIO_28	NC		
GPIO_29	SCL_3V3		

Ethernet LAN Port Pin Assignment

Ethernet LAN Port (RJ45 2.5G) JHH2			
Pin	Signal	Pin	Signal
1	NAPA2_PX_P_0	2	NAPA2_PX_N_0
2	NAPA2_PX_P_1	4	NAPA2_PX_P_2
3	NAPA2_PX_N_2	6	NAPA2_PX_N_1
4	NAPA2_PX_P_3	8	NAPA2_PX_N_3
5	GND	10	GND

Ethernet LAN Port (RJ45 2.5G) JH1			
Pin	Signal	Pin	Signal
1	NAPA0_PX_P_0	2	NAPA0_PX_N_0
2	NAPA0_PX_P_1	4	NAPA0_PX_P_2
3	NAPA0_PX_N_2	6	NAPA0_PX_N_1
4	NAPA0_PX_P_3	8	NAPA0_PX_N_3
5	GND	10	GND

UART Pin Assignment

UART Pin Assignment J4	
Pin	Signal
1	VDD_3V3
2	UART_RX
3	UART_TX
4	GND

Console PORT Pin Assignment

Console PORT Pin Assignment JHH3			
Pin	Signal	Pin	Signal
1	RTS_OUT	6	TX_OUT
2	DCD_IN	7	DTR_OUT
3	RX_IN	8	CTS_IN
4	GND	9	GND
5	GND	10	GND

M.2 Slot Pin Assignment

Top side		Bottom side	
1	NC	2	VDD_3V3
3	GND	4	VDD_3V3
5	GND	6	Card_Power_Off
7	USB_DP	8	3G_WAN_DISABLE
9	USB_DM	10	NC
11	GND		
Mechanical key B			
21	NC	20	NC
23	NC	22	NC
25	NC	24	NC
27	GND	26	GPS_DISABLE
29	USB0_SS_RXN	28	PLA_S2
31	USB0_SS_RXP	30	SIM_RST
33	GND	32	SIM_CLK
35	USB0_SS_TXN	34	SIM_I/O
37	USB0_SS_TXP	36	SIM_VCC
39	GND	38	NC
41	NC	40	SM2_CD
43	NC	42	SIM2_I/O
45	GND	44	SIM2_CLK
47	NC	46	SIM2_RST
49	NC	48	SIM2_VCC
51	GND	50	PCIE0_RSTn
53	PCIE_REFCLKN	52	PCIE0_CLK_REQ
55	PCIE_REFCLKP	54	PCIE_WAKE
57	GND	56	NC
59	NC	58	NC
61	NC	60	NC
63	NC	62	NC
65	NC	64	NC
67	RESET	66	SIM1_CD
69	NC	68	NC
71	GND	70	VDD_3V3
73	GND	72	VDD_3V3
75	NC	74	VDD_3V3
77	GND	76	GND
79	NC	78	NC
		80	GND

Top side		Bottom side	
1	GND	2	VDD_3V3
3	NC	4	VDD_3V3
5	NC	6	NC
7	GND	8	NC
9	NC	10	NC
11	NC	12	GND
13	NC	14	WSI_DAT_IN
15	NC	16	PCIE1_LED1
17	NC	18	GND
19	NC	20	WSI_CLK_IN
21	NC	22	GND
23	NC		
Mechanical key E			
33	GND	32	WSI_DAT_OUT
35	PCIE0_TX0_P	34	GND
37	PCIE0_TX0_N	36	WSI_CLK_OUT
39	GND	38	GND
41	PCIE0_RX0_P	40	GND
43	PCIE0_RX0_N	42	NC
45	GND	44	NC
47	REFCLK_P	46	NC
49	REFCLK_N	48	NC
51	GND	50	NC
53	CLKREQ_3V3	52	PERST_3V3
55	WAKE_3V3	54	NC
57	GND	56	WLAN_DISABLE
59	PCIE0_TX1_P	58	NC
61	PCIE0_TX1_N	60	NC
63	GND	62	NC
65	PCIE0_RX1_P	64	NC
67	PCIE0_RX1_N	66	NC
69	GND	68	NC
71	NC	70	GND
73	NC	72	VDD_3V3
75	GND	74	VDD_3V3