

Component Map



GPIO Pin Mapping

GPIO Pin	Function	GPIO Pin	Function
GPIO_0	WLAN_DISABLE_L	GPIO_1	LED_WLAN_L
GPIO_2	MCI_CLK_IN	GPIO_3	MCI_CLK_OUT
GPIO_4	MCI_DATA_OUT	GPIO_5	MCI_DATA_IN
GPIO_19	FEM_VRX		

Power Requirements

DC Power	3.3V
Power Consumption	5 Watts (Max)

miniPCIe Slot Pin Assignment

Top side		Bottom side	
1	WAKE_L	2	+3.3V
3	NC	4	GND
5	NC	6	NC
7	CLKREQ_L	8	MCI_CLK_IN (RESERVED)
9	GND	10	MCI_CLK_OUT (RESERVED)
11	REFCLK-	12	MCI_DATA_OUT (RESERVED)
13	REFCLK+	14	MCI_DATA_IN (RESERVED)
15	GND	16	NC
Mechanical key			
17	NC	18	GND
19	NC	20	W_DISABLE_L
21	GND	22	RESET
23	PERn0	24	+3.3V

Top side		Bottom side	
25	PERp0	26	GND
27	GND	28	NC
29	GND	30	NC
31	PETn0	32	NC
33	PETp0	34	GND
35	GND	36	NC
37	GND	38	NC
39	+3.3V (RESERVED)	40	GND
41	+3.3V (RESERVED)	42	NC
43	GND	44	LED_WLAN_L
45	NC	46	BT_LED
47	test point	48	NC
49	NC	50	GND
51	NC	52	+3.3V